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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10082648	02/22/2002	438	694	1765	DeD.

**APPLICANTS: Chen Tze-Chiang; Han Liang-Kai;

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		FIS920010243US1
Verified and Acknowledged Examiners's initials		

TITLE : Deep trench isolation of embedded DRAM for improved latch-up immunity

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

CLAIMS ALLOWED

Assistant Examiner		Total Claims	Print Claim for O.G.
Primary Examiner		DRAWING	
Application Examiner		Sheets Drwg.	Figs. Drwg.
PREPARED FOR ISSUE		Print Fig.	
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ISSUE FEE

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